Amendments to the Specification:

Please amend the following paragraph beginning at page 2, line 19 as follows:

5 U.S. patent No. 5,961,653 to Kalter et al. discloses a microprocessor based BIST for an embedded memory; however, the complication and density impact of including a microprocessor on an on-chip macro makes this approach inefficient and ~~impractable~~ impractical.